h. Late , MAR 2 1 2002

INFORMATION DISCLOSURE STATEMENT

Serial Number **Docket Number** 1305.4US01 09/849,913 RECEIVED Applicant(s) Sarkinen et al. Group Art Unit Filing Date

## PTO Form 1449 MAR 2 2 2002 2861 265 05/04/01 Technology Center 2600 **U.S. PATENT DOCUMENTS EXAMINER FILING DATE** REF DOCUMENT NUMBER CLASS SUB-CLASS DATE NAME (IF APPROPRIATE) INITIALS 6,175,568 01/16/01 Awdeh PPQ 6,167,445 12/26/00 Gai et al. 6,166,403 12/26/00 Castagnetti et al. Pol 6,141,686 10/31/00 Jackowski et al. P.H 6,136,638 10/24/00 Lee et al. bn 06/06/00 Witters et al. 6,072,989 Du DH 6,047,002 04/04/00 | Hartmann et al. DH 6.046,980 04/04/00 | Packer 6,046,979 04/04/00 Bauman nu DII 6,032,190 02/29/00 Bremer et al. 5,995,439 11/30/99 Watanabe et al. DH 5,973,952 10/26/99 Crafts KØ 08/24/99 Wakeland 5,943,481 DH 5,923,596 07/13/99 Wu et al. Dil 5,907,511 05/25/99 Crafts Dri 5,901,095 05/04/99 Crafts Dii 04/20/99 | Wakeland 5,896,383 DH 10/27/98 Takase et al. m 5,828,654 βŪ 5,812,476 09/22/98 Segawa DH 5,764,641 06/09/98 Lin 5,666,353 09/09/97 Klausmeier et al. DH DH 5,600,598 02/04/97 Skjaveland et al. 5,598,410 DH 01/28/97 Stone 5,566,170 Dit 10/15/96 Bakke et al. 9H 5.541.920 07/30/96 | Angle et al. **FOREIGN PATENT DOCUMENTS** TRANSLATION EXAMINER REF **DOCUMENT NUMBER** DATE CLASS SUB-CLASS COUNTRY INITIALS YES **OTHER DOCUMENTS** "Frame Based ATM Over SONET/SDH Transport (FAST)," The ATM Forum, Technical Committee, D71 fb-fbatm-0151.000, July 2000, 37 pgs. William Wong, "Network Processors Take The High Road... And The Low Road," Electronic Design, July 10, 2000, http://www.planetee.com/planetee/servlet/DisplayDocument?ArticleID=6798, Retrieved DH June 15, 2001, 3 pgs. "Network Processors Take The High Road... And The Low Road," Electronic Design, July 10, 2000, DH http://www.planetee.com/planetee/servlet/DisplayDocument?ArticleID=6799, Retrieved June 15, 2001, 2 pgs.

Examiner:	Duc Ho	 Date Considered:	1-21-05

Page 2 RECEIVED

	MAR 2 1 ZULL LI		- CLIVE				
	MARITOME	Docket Number	Serial Number				
	(A)	1305.4US01	99/849,913 MAR 2 2 2002				
INFOR	RMATION DISCLOSURE CTATEMENT	Applicant(s)	Technology Center 2				
	PTO Form 1449	Sarkinen et al.	IECHNOlogy Center of				
		Filing Date	Group Art Unit				
		05/04/01	2681 7665				
	"Network Processors Take The High Road And The Low Road," Electronic Design – July 10, 2000,						
DH	http://www.planetee.com/planetee/servlet/DisplayDocument?ArticleID=6800, Retrieved March 7, 2002, 3						
	pgs.						
DH	"Network Processors Take The High Road And The Low Road," Electronic Design, July 10, 2000,						
	http://www.planetee.com/planetee/servlet/DisplayDocument?ArticleID=6802, Retrieved June 15, 2001, 1 pg.						
1.12	"Network Processors Take The High Road And The Low Road," Electronic Design, July 10, 2000,						
DH	http://www.planetee.com/planetee/servlet/DisplayDocument?ArticleID=6804, Retrieved June 15, 2001, 1 pg.						
ba.	"Network Processors Take The High Road And The Low Road," Electronic Design, July 10, 2000,						
DH	http://www.planetee.com/planetee/servlet/Dis	anetee.com/planetee/servlet/DisplayDocument?ArticleID=6806, Retrieved June 15, 2001, 1 pg.					
- <del> </del>	"Network Processors Take The High Road And The Low Road," Electronic Design, July 10, 2000,						
DH	http://www.planetee.com/planetee/servlet/DisplayDocument?ArticleID=6808, Retrieved June 15, 2001, 1 pg.						
	"EZchip Technologies Completes Filing Patent Applications For Its 10/40G Network Processor Core						
DH	Technology," EZchip – Press Release, June 8, 2000, http://www.ezchip.com/html/press_000918.html,						
Day 1	printed January 22, 2001, 3 pgs.						
	"7-Layer Packet Processing: A Performance Analysis, White Paper," EZchip,						
DH	http://www.ezchip.com/html/tech_7layers.html, Retrieved January 22, 2001, 8 pgs.						
	"Network Process Designs for Next-Generation Networking Equipment, White Paper," EZchip,						
PH	http://www.ezchip.com/html/tech_nsppaper.html, Retrieved January 22, 2001, 8 pgs.						
<del></del>							
DH	"GILDER TECHNOLOGY REPORT," EZchip, September 2000, http://www.ezchip.com/html/gilder.html,						
	Retrieved January 22, 2001, 2 pgs.						
· KO	"Putting Routing Tables in Silicon," IEEE NETWORK, Vol. 6, No. 1, January 1992, 11 pgs.						
DH	Bossardt et al., "ABR Architecture and Simulation for an Input-Buffered and Per-VC-Queued ATM						
	Switch," Department of Electrical and Computer Engineering, University of Illinois, 6 pgs.						
DH	"C-5™ Digital Communications Processor," Product Brief, C-PORT, A Motorola Company, Date						
	Unknown, 8 pgs.						
Ka	David Husak, "Network Processors: A Definition and Comparison," C-PORT, A Motorola Company,						
	Date Unknown, 8 pgs.						
DH	"Products," Applications, C-PORT, A Moto	rola Company.					
	http://www.cportcorp.com/products/applications.htm, Retrieved January 23, 2001, 3 pgs.						
<del></del>	Husak et al., "Network Processor Program						
DH	and Extending Product Life," C-PORT, A						
	Tand Extending Floudet Life, C-PORT, AT	violorola Company, Ma	ay 4, 2000, 0 pgs.				

	Examiner:	٨٥	Date Considered:	
l	Duc o	110	Date Considered.	1-21-03